

<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>	Docket Number (Optional) <b>NSC1-G0810  P04233 D1 </b>	Application Number <b>New</b>
	Applicant(s) <b>Charles Dark et al..</b>	
	Filing Date <b>Herewith</b>	Group Art Unit <b>Unknown</b>

### U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	AA	* 4,130,883	12/19/78	Hazelton	364	900	10/14/75
	AB	* 5,951,660	09/14/99	Van Wonteghem	710	103	12/10/97
	AC	* 5,506,991	04/09/96	Curry et al.	395	800	12/19/90
	AD	* 3,961,326	06/01/76	Craven	340	347	09/12/74
	AE	* 4,725,836	02/16/88	Guidos	340	825.590	01/27/86
	AF	* 4,589,106	05/13/86	Prather et al.	370	58	08/20/84
	AG	* 4,764,939	08/16/88	Rogers	375	37	08/19/87
	AH	* 6,502,143	12/31/02	Bass et al.	710	1	12/31/99
	AI	* 5,956,523	09/21/99	Chen	395	882	08/09/96
	AJ	* 4,497,041	01/29/85	Braun	364	900	09/09/82
	AK	* 5,812,879	09/22/98	Moro	395	882	04/19/93
	AL	* 5,844,829	12/01/98	Freidin et al.	364	716.03	12/04/97
	AM	* 5,901,221	05/04/99	Eriksson et al.	379	399	11/08/95
	AN	* 5,596,637	01/21/97	Pasetti et al.	379	399	05/02/94
	AO	* 5,701,103	12/23/97	Fujii	330	290	07/10/96
	AP	* 4,491,804	01/01/85	Main et al.	330	265	11/18/82
	AR	* 4,489,283	12/18/84	Ishizaki	330	268	01/28/82
	AS	* 4,356,452	10/26/82	Iwamura	330	265	08/26/80
	AT	* 4,833,423	05/23/89	Molloy	330	265	07/20/87
	AU	* 4,509,113	04/02/85	Heath	364	200	02/02/82
	AV	* 5,432,698	07/11/95	Fujita	364	413.02	05/31/94
	AW	* 4,695,955	09/22/87	Faisandier	364	413	12/26/84

### FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

### OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AX	* Ossman, M., "Single µC Pin Makes Half-Duplex RS-232C", Electronic Design News, pp. 118, August 5, 1999
AY	* "COP8SA Family 8-Bit CMOS ROM Based and One-Time Programmable (OTP) Microcontroller with 1K to 4K Memory, Power on Reset, and Very Small Packaging", National Semiconductor Corporation, Preliminary July 1999 (DS012838)
AZ	* "PC16550D Universal Asynchronous Receiver/Transmitter with FIFOs", National Semiconductor Corporation, TL/C/8652, June 1995
BA	* "DSV14196 +3.3V Supply EIA/TIA-232 5 Driver x 3 Receiver", National Semiconductor Corporation, DS100853, April 1999
BB	* Campbell G.W., "Low Power RS-232C Driver and Receiver in CMOS", National Semiconductor Corporation, AN008681, Application Note 438, July 1986
BC	* Goldie J., "Summary of Well Known Interface Standards", National Semiconductor Corporation, AN005855, Application Note 216, July 1998
BD	* Horowitz, P. et al., excerpts from "The Art of Electronics", Second Edition, Digital Signals and Long Wires, pp. 603; Digital Meets Analog, pp. 604; Low-Power Design, pp. 984; Self-Explanatory Circuits pp. 985, 1980, 1989
BE	* John F. Wakerly, Digital Design Principles & Practices, 1990, Prentice Hall, 3rd edition, pp. 157-158
BF	* ARC Electronic, RS232 Interface, 1999, <a href="http://www.arcelect.com/rs232.htm">http://www.arcelect.com/rs232.htm</a>
BG	* ELM Electronics, ELM 624-Control L to RS232 Interpreter, 1999, <a href="http://www.elmelectronics.com">http://www.elmelectronics.com</a>

	BH	* ELM Electronics, ELM621-Serial Three Bit Port, 1999, <a href="http://www.elmelectronics.com">http://www.elmelectronics.com</a>
	BI	* TechEncyclopedia, PMOS definition, <a href="http://www.techweb.com/encyclopedia/defineterm?term=pmos">http://www.techweb.com/encyclopedia/defineterm?term=pmos</a>

Examiner	<i>Jun Li</i>	Date Considered	1.17.06
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			